

Analog Integ Circuit Dgn

Last Updated: Mon, 03/30/2026

Course prefix: ECE

Course number: 6412

Section: A

CRN

57106

Instructor first name: Gabriel

Instructor last name: Rincon

Semester: Summer

Academic year: 2026

Catalog description: Design of analog circuits using CMOS and bipolar technologies.

Academic honesty/integrity statement:

Students are expected to maintain the highest standards of academic integrity. All work submitted must be original and properly cited. Plagiarism, cheating, or any form of academic dishonesty will result in immediate consequences as outlined in the university's academic integrity policy.